### **ASHFAQUE AHMED**

H.No.236, Lane no 02, Islampura, Malegaon Dist Nashik-423203

Mobile: 7020960416 Email:an.ashfaque2010@gmail.com

#### **Objective:**

To become a successful professional in the field of Electronics and Telecommunication and to work in an innovative and competitive world.

# **Work Experience:**

#### > Teaching Experience: (11 yrs)

1.

• Name of College: Maulana Mukhtar Ahmad Nadvi Technical Campus, Mansoora,

• Designation : Assistant Professor

• Location : Near Manmad Chawfully, Mansoora, Malegaon (Nashik)-423203

• Duration : 14<sup>th</sup> July 2016 to till date

• Subjects Taught: Digital Circuits (DC), Power electronics (PEL), Control systems (CS), Digital Signal Processing (DSP), Signals and Systems (SS), PLC & Automation (PLC & A), Information Theory & Coding Techniques (ITCT), Broadband Communication Systems (BCS).

2.

• Name of College : K.B.H.S.S.T's Polytechnic

• Designation : Lecturer

• Location : Bhaygaon Road, Malegaon Camp, Malegaon. (Nashik)-423203

• Duration : 29<sup>th</sup> Aug 2012 to 9th May 2015

• Subjects Taught: Power electronics (PEL), Mobile Communication (MCN), Control systems (CSS).Linear Integrated circuits (LIC), Electronic Instruments & Measurements (EIM). AC Machines, Energy conservation and Audit (ECA).

# > Industry Experience: (3 yrs)

1. Name of Company: TATA Communicatins Limited.

Designation : Executive-Global Access Management (GAM)

Location : BKC, Mumbai

Duration : 21<sup>st</sup> July 2011 to 27<sup>th</sup> Aug 2012

2. Name of Company: TATA Teleservices Limited

Designation : Programme Manager

Location : Mumbai

Duration : 5th April 2010 to 20<sup>th</sup> July 2011

3. Name of Company: Tifra Telecom Constructions & Services

: Trainee Engineer Designation

Location

: Mumbai : 5<sup>th</sup> August 2009 4<sup>th</sup> April 2010 Duration

# **Education:**

| COURSE   | YEAR OF<br>PASSING | BOARD<br>/UNIVERSITY                    | PERCENT | GRADE                        |  |
|--|--------------------|---|---------|------------------------------|--|
| PH.D.(ELECTRONICS & COMMUNICATION ENGINEERING) | Pursuing           | Oriental University,<br>Indore          |         | ŀ                            |  |
| M.E. (VLSI AND EMBEDDED SYSTEM DESIGN)         | 2018               | North Maharashtra<br>University,Jalgoan | 72.90%  | First Class with Distinction |  |
| B.E.(E & TC)                                   | 2009               | North Maharashtra<br>University,Jalgoan | 69.13%  | First Class with Distinction |  |
| HSC  | 2005               | Nashik Divisional<br>Board              | 81.50%  | First Class with Distinction |  |
| SSC  | 2003               | Nashik Divisional<br>Board              | 84.66%  | First Class with Distinction |  |

**Publication:** Total 10 papers published in Journals and Conferences.

| Sr.no | Title   | Conference/<br>Journal | Name Of<br>Journal<br>/Conference | Year |
|-------|---|------------------------|-----------------------------------|------|
| 1     | IoT based performance analysis of water level control using NS2                                 | Conference             | ICSTS-2022                        | 2022 |
| 2     | Application of Internet of Things in Multiple Tanks water level controlling                     | Conference             | I-CREATE 2020                     | 2020 |
| 3     | Mobile Ad hoc Network Routing Protocol<br>Design and Testing using NS2                          | Conference             | I-CREATE 2020                     | 2020 |
| 4     | Current Mode Full Wave Rectifier Topology for Integration                                       | Conference             | I-CREATE 2020                     | 2020 |
| 5     | Signal Integrity issues Reduction in PCB using Meta Material                                    | Conference             | I-CREATE 2020                     | 2020 |
| 6     | IOT based Mutiple Tanks Water Level<br>Monitoring and Control                                   | Journal                | IOSR Journal                      | 2019 |
| 7     | A DSP based Power Factor correction scheme for switching power supplies                         | Journal                | IOSR Journal                      | 2019 |
| 8     | Performance Analysis of AODV & DSR routing protocols using NS2 Simulator                        | Journal                | IOSR Journal                      | 2019 |
| 9     | A Review on current mode CMOS<br>Multiplier circuit with Improved accuracy<br>using Analog VLSI | Journal                | IOSR Journal                      | 2019 |
| 10    | Multilevel Water Level Control and<br>Monitoring Of Multiple Tanks                              | Journal                | IJASRE                            | 2018 |

## **Personal Profile:**

Father's Name: Khaleeque Ahmed

Date of Birth: 20-08-1987.

Aadhar no: 5922 2700 4069

PAN no: BCMPK8242D

Sex: Male

Marital Status: Married

Languages Known: English, Hindi, and Urdu

Permanent Address: 236, Lane no.2, Islampura, Malegaon,

Dist-Nashik423203 (M.S.)

Nationality India

## **Declaration:**

I consider myself familiar with Electronics and Telecommunication Engineering Aspects. I hereby declare that the information furnished above is true to the best of my knowledge.

Date:

Place: Malegaon (Ashfaque Ahmed)