Resume

Shahzad Mobeen Ahmed

M.E. (Electronics), Ph.D (Pursuing)

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OBJECTIVES:

Considering success as a process and not an event, I continuously invest time to develop the positive attitude to expose the best of me in the demanding education world.

EDUCATIONAL QUALIFICATION:

Examimation	Board/ University	Year of Passing	% of Marks	Class
Ph.D	Pune	Pursuing	---	---
M.E. (Electronics)	Shivaji University Kolhapur	Jan 2010	65.94	1st Class
B.E. (Electronics)	Pune University	Aug 1997	64.73	1st Class
Diploma in Industrial Electronics (D. I. E.)	B.T.E. Maharashtra	July 1994	62.81	1st Class
S.S.C.	Pune	June 1991	88.00	Distinction

WORKING EXPERIENCE (TOTAL 25 YEARS):

1) Working as Assistant Professor in Maulana Mukhtar Ahmad Nadvi Technical Campus (Pune University) since 22nd July 2015 to till date.

2) Worked as a Assistant Professor in S.R.E.S. College of Engineering Kopargaon. (Pune University) from 1st April 2010 to 21st July 2015.

3) Worked as a Sr. Lecturer in S.R.E.S. College of Engineering Kopargaon. (Pune University) from 1st April 2009 to 31st March 2010.

4) Worked as a Lecturer in S.R.E.S. College of Engineering Kopargaon. (Pune University) from 19th Jan 2000 to 31st March 2009.

5) Worked as a Lecturer at K.B.H.S.S.T.'s Polytechnic, Malegaon Camp Dist.-Nasik from 1st Sep 1998 to 8th Oct 1999.

SOFTWARE SKILLS:

- **Operating Systems:** Worked on DOS, Windows 98, Windows XP, Windows 7, Windows 8, Linux,
- **Languages:** Worked on C, C++, Embedded C ALP (MASM, TASM), Embedded RTOS using C, Visual Basic and VHDL.
- **Software's**: Worked on Protel, PCPRO, 8051 Simulation Package (Keil), MPLAB, Xilinx ISE, Microwind, Cadence, MATLAB, SCILAB, ALTERA, ModelSim, MultiSim, Hspice, Circuit Maker, Proteus & Code Composer Studio.

SUBJECTS TAUGHT:

1) Microelectronics (First Year M.E. Electronics)
2) Digital System Design using PLDs (First Year M.E. Electronics)
3) PLDs & ASICs (First Year M.E. Electronics)
4) Power Electronics I & Power Electronics II (T.E. & B.E. Electronics)
5) VLSI Design & Technology (B.E. E&Tc.)
6) Industrial Drives and Control (B.E. E&TC.)
7) Optical Fiber Communication (B.E. E&Tc.)
8) Optical & Microwave Communications (B.E. E&Tc.)
9) Advanced Microprocessor (T.E. E&Tc.)
10) Embedded System Design (B.E. Electronics)
11) Network Theory (T.E. Electronics)
12) Digital Electronics (S. E. E&Tc.)
13) Networks and Line (S.E. Electronics)
14) Analog Circuits (S.E. Computer)
15) Electronic Devices and Circuits (S.E. Electronics)
16) Elements of Electronics Engineering (F.E.)
17) Basic Electronics Engineering (F.E.)
18) Basic Electrical Engineering (F.E.)

ADDITIONAL RESPONSIBILITIES HELD:

1) Worked as HOD of E&TC department from 22nd July 2015 to November 2020.

2) Worked as HOD of Electrical department from 22nd July 2015 to May 2018.

3) Worked as HOD of Applied Science department and F.E. in charge from 22nd July 2015 to November 2020.

4) Worked as In Charge Principal of MMANTC from December 2020 to December 2021.

5) Worked as admission in charge and Dean Admissions of MMANTC from 22nd July 2015 to December 2023.

6) Working as officer in charge of MSBTE Exam section of MMANTC since Jan 2024 to till date.

7) Worked as **P. G. Coordinator** from 1st July 2012 to 21st July 2015.

8) Worked as **Secretary** of SRES COE Alumni Association (Registered body) from 1st June 2010 to 21st July 2015.

9) Worked as **Lab Incharge** of research Lab from 1st July 2012 to 21st July 2015.

10) Worked as **Incharge of load distribution** in consultation with HOD of E & Tc. Department from 1st June 2006 to 21st July 2015.

11) Worked as **Internal Auditor** for ISO 9001-2008 from 1st June 2011 to 21st July 2015..

12) Worked as **Time Table Incharge** of E & Tc. department from 1st June 2006 to 21st July 2015.

13) **Set up** Power Electronics Lab, VLSI Design Lab, PCB Lab and Research Lab.

14) **Prepared Experimental kits** and set up (Approximately 30 kits) for subjects Power Electronics, Industrial Drives & Control, Basic Electronics Engineering, Electronics Devices & Circuits, Network Analysis and VLSI Design etc.

15) **Prepared Lab Manuals** for subjects VLSID&T, OFC, VLSI Design, O&MC, PE, BEE, NA, EDC etc.

16) Worked as **Incharge** for preparation of **LIC report** for the **proposed research center** of SRES COE Kopargaon and successfully presented it in front of LIC committee for research center.

17) Worked as **Co-Incharge** for preparation of **NBA report** of SRES COE Kopargaon in 2012.

18) **Organized** Alumni meet at Pune, Nashik, Banglore, Delhi, Shirdi and Kopargaon.

19) **Conducted** software course for college students on Xilinx and Altera EDA Tools.

20) **Conducted expert lecture on** Optical Fiber Communication at SND, COE&RC, Yeola and

software course on Xilinx and Altera EDA Tools at COE Chincholi.

21) **Resource person and key note speaker** for one week STTP on VLSI Design at PREC, Loni.

22) Worked as **Lab Incharge** of VLSI Design Lab from 1st July 2006 to 30th June 2012.

23) Worked as **Lab Incharge** of PCB Lab from 1st July 2003 to 30th June 2006.

24) Worked as **Coordinator** for organization of Techno-vision 2012, a state level competition for UG & PG students for paper presentation, program writing and circuit debugging.

25) Worked as **Executive Committee Member** of ISTE Student Chapter SRES COE Kopergaon from 1st July 2006 to 30th June 2009.

26) Worked as **Executive Committee Member** for SRES COE Alumni Association (Registered body) from 1st July 2005 to 31st May 2010.

27) Worked as **Event Incharge** of Dance competition program during college annual social gathering "SANJIVAN 2014".

28) Worked as an **Event Inchrge** of Personality Contest during college annual social gathering "SANJIVAN 2010".

29) Worked **as Incharge and Member** of different committees such as technical committee, catering committee, stage committee etc. for national and state level paper presentation and competitions held by SRES COE Kopargaon during last eight years.

PAPER PRESENTATION & PUBLICATIONS: (Total: 16)

Publications: (Total: 07)

1) "Comparison Analysis of Specific Absorption Rate (SAR) for Hand Held Mobile Phone Operating at 4G & 5G ", International Conference on Science, Technology and Sustainability (ICSTS-2022)

2) "Design of Memristor Based Full Adder Circuits", IJAST, vol.29, no. 9s, pp. 7915-7925 june 2020.

3) "Current Mode Full Wave Rectifier Topology for Integration", IJAST, vol.29, no. 9s, pp. 7608-7614 june 2020.

4) "IOT based Power loom Monitoring System", IJAST, vol.29, no. 9s, pp. 7732-7741 june 2020.

5) "Implementation of Full Adders using Different technologies and its Comparison", Elsevier Publication, ISBN No.: 978-93-5107-221-8, March 2014.

6) "A Can Bus Based Fault Monitoring And Diagnosis System InWind Turbine", Elsevier Publication, ISBN No.: 978-93-5107-221-8, March 2014.

7) "Human IRIS recognition and effect of compression in recognition system", IJECSCSE Publication, ISBN No.: 978-81-926080-0-6, February, 2013.

Paper presentation in International Conferences: (Total: 05)

1) "Implementation of Full Adders using Different technologies and its Comparison", paper presented in 3rd International Conference on Recent Trends in Engineering & Technology, (ICRTET'2014) at SNJB's College Of Engineering, Chandwad. (28-30 March 2014).

2) "A Can Bus Based Fault Monitoring And Diagnosis System InWind Turbine", paper presented in 3rd International Conference on Recent Trends in Engineering & Technology, (ICRTET'2014) at SNJB's College Of Engineering, Chandwad. (28-30 March 2014).

3) "Human IRIS recognition and effect of compression in recognition system", paper presented in 2nd International Conference on Recent Trends in Engineering & Technology, (ICRTET'2013) at SNJB's College Of Engineering, Chandwad. (22-24 February 2013).

4) "A DSP Based Power Factor Correction Scheme for Switching Power Supplies" paper presented in ICSES-2010 at SES COE, Navalnagar. (April 01-02 2010).

5) "Power Measurement Using Digital Signal Processing technique" paper presented in ICSES-2010 at SES COE, Navalnagar. (April 01-02 2010).

Paper presentation in National Conferences: (Total: 04)

1) "Multiple DC Motors Control Using DSP TMS320LF2407A", paper presented in "TECHNOSPIRE 2014" at SRES COE Kopargaon. (10-12 January 2014)

2) "A Digital Power Factor Correction Scheme for SMPS Using Microcontroller" paper presented in MVP-NCEC-2010 NDMVP COE Nashik. (March 25-27 2010).

3) "Power Measurement Using Digital Signal Processing technique" paper presented in MVP-NCEC-2010 NDMVP COE Nashik. (March 25-27 2010).

4) "An Effective Call Admission Control for Wireless Ad Hoc Network" paper presented in NCECA-08 at COE, Pandharpur. (23 & 24 Dec 2008 Page no.51-55).

SHORT TERM TRAINING PROGRAM (STTP) ATTENDED: (Total: 11)

1) Attended one week STTP on "MATLAB Applications in Electronics & Telecommunication

Engineering" at TKIET, Warananagar, Kolhapur during 13/12/2009 to 19/12/2009.

2) Attended three day workshop on "Cadence EDA Tool" conducted by Entuple Technologies Pvt. Ltd., at Matoshri COE & RC, Nashik from 04/10/2013 to 06/10/2013.

3) Attended two day workshop on "Research Methodology" at BS, COE, Pune, from 14/11/2014 to 15/11/2014.

4) Attended two day workshop on "Reconfigurable Hardware Lab" conducted by e-Prayog Virtual Labs, IIT Bombay at SRES, COE Kopargaon, from 04/02/2012 to 05/02/2012.

5) Attended one day workshop on "Research Methodology" at SRES, COE, Kopargaon on 27/11/2014.

6) Attended one day workshop on "VLSI Design" conducted by Cadence Design systems at Hotel Courtyard by Marriott, Pune on 23/08/2013.

7) Attended one day workshop on "Research Opportunities in VLSI Design" at PDVVP, COE, Ahmednagar on 10/04/2012.

8) Attended one day workshop on "Instructional Methodologies of Optical Fiber Communication & Process Automation" at DCOE&R, Pune on 07/01/2012.

9) Attended one day workshop on "Effective research Methodology" at SRES, COE, Kopargaon on 31/01/2010.

10) Attended one day workshop on "Syllabus Exploration of VLSI design" at Sinhgad COE, Pune, on 11/07/2006.

11) Attended one day workshop on "Digital Signal Processing – Hardware Approach" at VIT, Pune, on 18/03/2005.

PROJECTS UNDERTAKEN: (Total: 04)

1) Research work for Ph.D on "Design and Implementation of Full Adder with Memristor to improve the performance in terms of speed, power consumption, PDP and area utilization".

2) "Digital (DSP) Power Factor Correction Using Boost Converter For SMPS"(In M.E. Electronics).

3) " Microprocessor Based LCR Meter " (In B.E. Electronics)

4) " Digilo Scope " (In Diploma in Industrial Electronics)

PROJECTS GUIDED: (Total: 33)

P. G.: (Total: 06)

1) "Industrial Process Parameter Control using Ethernet" (2014-15 in progress)

2) "Wireless Communication using Light Fidelity (LiFi)" (2014-15 in progress)

3) "Design and Simulation of Full Adder using Different Logics" (2013-14)

4) "DSP Based Multiple DC Motors Controller" (2013-14)

5) "Mango Leaves Disease Severity Measurement using Image Processing Technique" (2013-14)

6) "Human IRIS Recognition and Effect of Compression on Recognition System" (2012-13)

U. G.: (Total: 27)

1) "Wireless Speed and Direction Control of AC & DC Motors" (2013-14)

2) "Automatic Phase Selector" (2013-14)

3) " FPGA Implementation of Controller Design for Remote Sensing System" (2013-14)

4) "Automatic Energy Meter" (2012-13)

5) "Guard Tour Petrol System" (2012-13)

6) "Robot for Fire Accident Rescue" (2012-13)

7) "Multichannel Interlocking System using CPLD" (2011-12)

8) "VLSI Based Snap Taker of Signal Breaker" (2011-12)

9) "Fruit Separation System using MATLAB" (2010-11)

10) "Control of Three Phase Fully Controlled Converter using FPGA" (2009-10)

11) "FPGA based Single Phase Induction Motor Drive using PWM Inverter" (2009-10)

12) "Ticket Fair Management using E-Ticket" (2008-09)

13) "9 – step Speed Control of DC Shunt Motor" (2008-09)

14) "Speed Control of DC Motor using Two Quadrant Chopper" (2008-09)

15) "Microcontroller Based Weather Monitoring Station" (2008-09)

16) " Microcontroller Based 12V DC to 230V AC Power Inverter" (2007-08)

17) "DSP Based Power Correction using Boost Converter for SMPS" (2007-08)

18) "Microcontroller Based Speed Control of Separately Excited DC Motor using Semiconverter" (2007-08)

19) "Microcontroller Based Power Factor Correction using Boost Converter" (2007-08)

20) "DSP Based Power Correction" (2006-07)

21) "Microcontroller Based Power Factor Improvement" (2006-07)

22) Transistorized Speed Control of Single Phase Induction Motor" (2005-06)

23) "Three Phase Square Wave Bridge Inverter" (2005-06)

24) "Three Phase Square Wave Inverter" (2004-05)

25) "Speed Control of Single Phase IM using PWM Technique" (2004-05)

26) "Gasket Magnetizer" (2003-04)

27) "Microcontroller Based Three Phase Power Measurement and Control" (2003-04)

RESPONSIBILITIES HELD AT UNIVERSITY LEVEL:

1) Worked for syllabus formation, detailing and preparation of teaching methodology for different subjects such as Microelectronics (M.E. Electronics), PLDs & ASICs (M.E. Electronics), VLSI Design & technology (B.E. E&Tc.), Optical Fiber Communication (B.E. E&Tc.), Power Electronics (T.E. E&Tc.), Basic Electronics Engineering (F.E.) and Network Theory (T.E. E&Tc.).

2) Session chair for ePGCON 2014 for M.E. Dissertation presentations.

3) University exam paper setter for Microelectronics (M.E. Electronics) 2008 & 2013 Pattern.

4) Paper Checking for Microelectronics (M.E. Electronics), VLSI Design & Technology (B.E. E&Tc.), Optical Fiber Communication (B.E. E&Tc.), Digital Electronics (S.E. E&Tc.) and Basic Electronics Engineering (F.E.) etc.

5) Internal and external Senior Supervisor for university theory exam.

6) Internal and external examiner for practical & oral exams for different subjects at M.E., B.E., T.E. & S.E. level.

ACHIEVEMENTS:

1) Got admission in Ph.D at Matoshri COE&RC Nashik, in academic year 2014-2015.

2) Qualified as Certified Internal Auditor of ISO 9001-2008 after training and examination for auditing quality in educational institutes on 30/09/2011.

3) Got appreciation letter from management of SRES, COE, Kopargaon for final exam result of subject Optical and Microwave Communication taught in B.E. (E&Tc.) in second semester of academic year 2010-2011.

4) Got appreciation letter from management of SRES, COE, Kopargaon for final exam result of subject Basic Electronics Engineering taught in F.E. in second semester of academic year 2010-2011.

5) Got appreciation letter from management of SRES, COE, Kopargaon for final exam result of

subject VLSI Design and Technology taught in B.E. (E&Tc.) in first semester of academic year 2011-2012.

MEMBERSHIPS:

1) LMISTE

PERSONAL DETAILS:

- Name : Shahzad Mobeen Ahmed
- Date of birth : 1st June, 1975
- Sex : Male
- Marital Status : Married
- Religion : Islam
- Nationality : Indian
- Languages : English, Hindi, Marathi, Urdu
- Permanent Address: Shahzad Mobeen Ahmed
  412/B, Guruwar Ward , Belbaugh, Malegaon.
  Dist.- Nashik.   423203

I hereby declare that the information furnished above is true to the best of my knowledge.

Shahzad Mobeen Ahmed